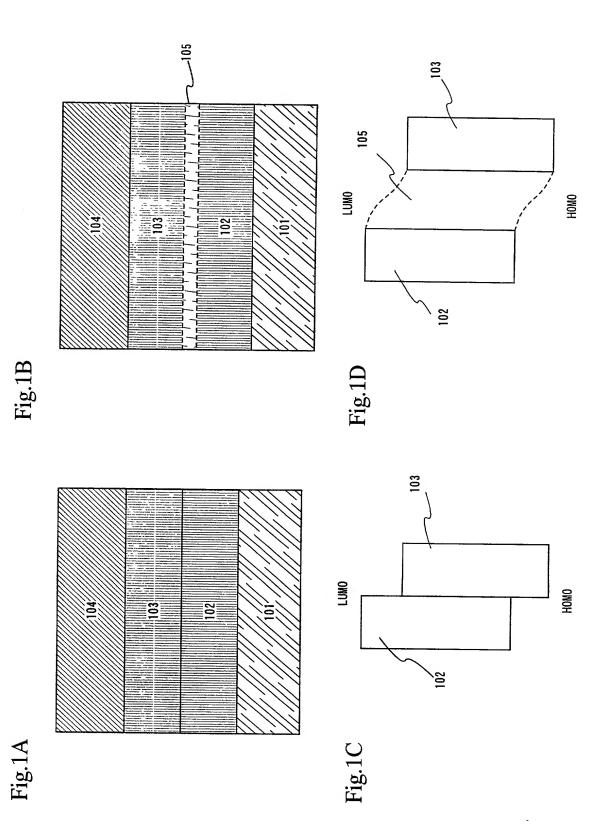
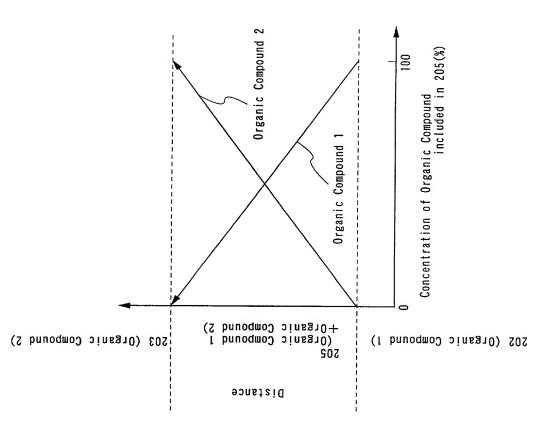
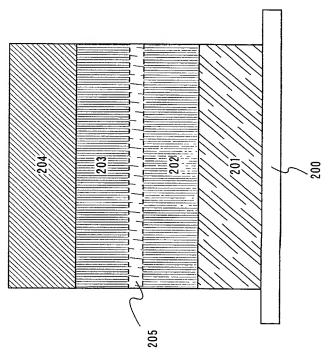
Page 1 of 37



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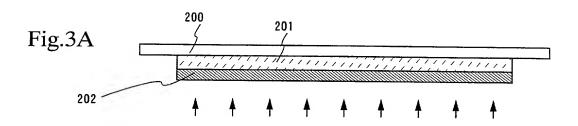
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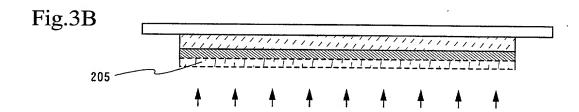


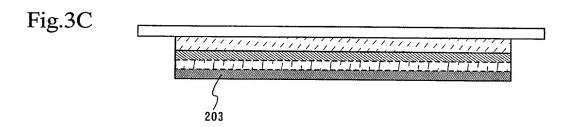


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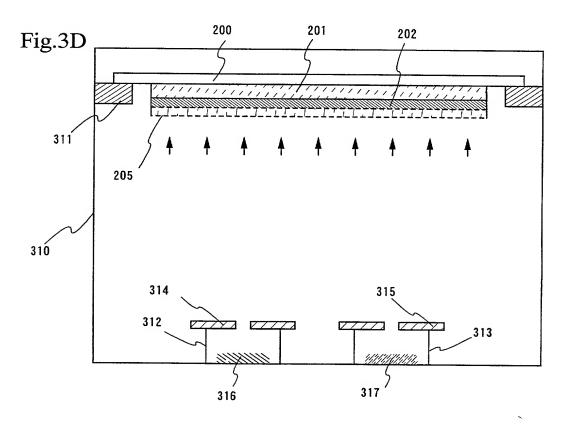


Fig.4

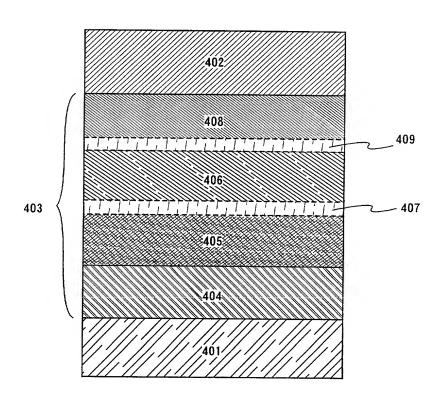
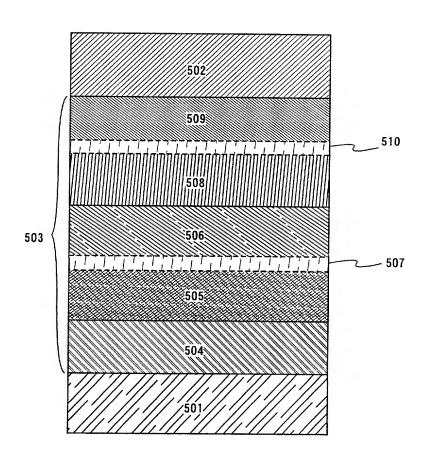
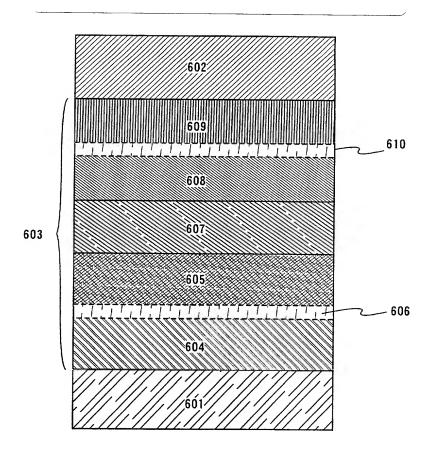
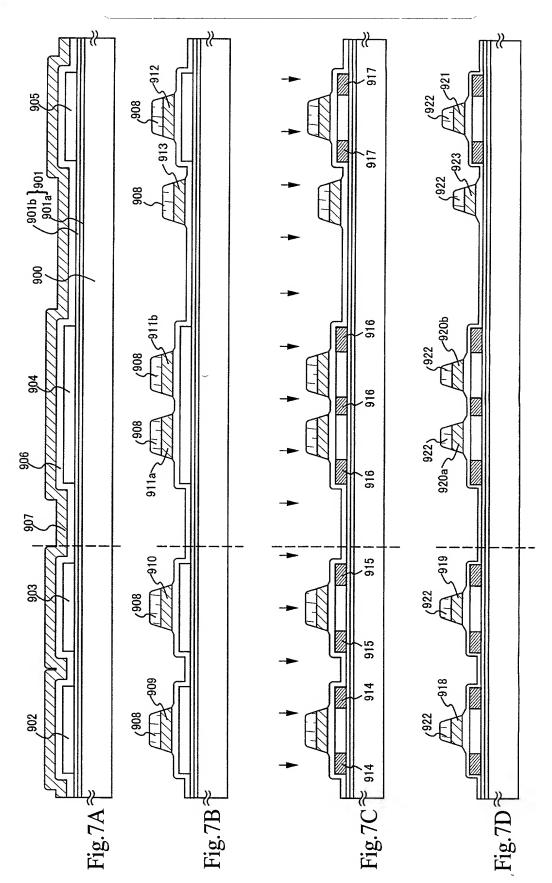


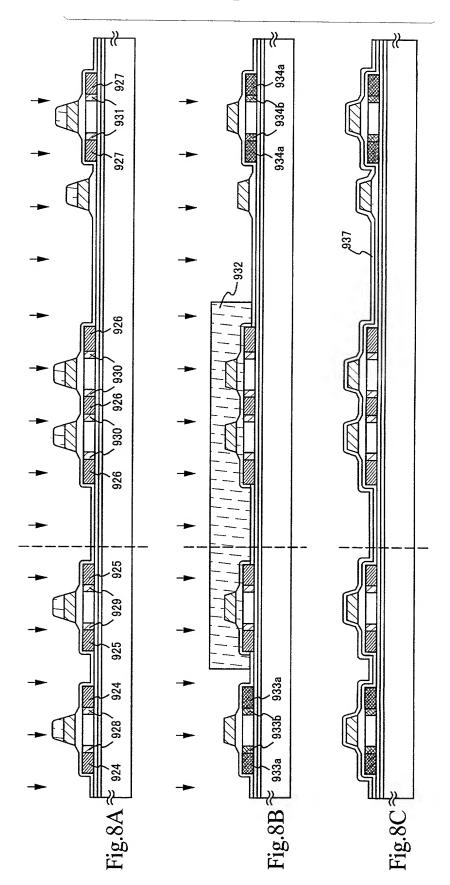
Fig.5

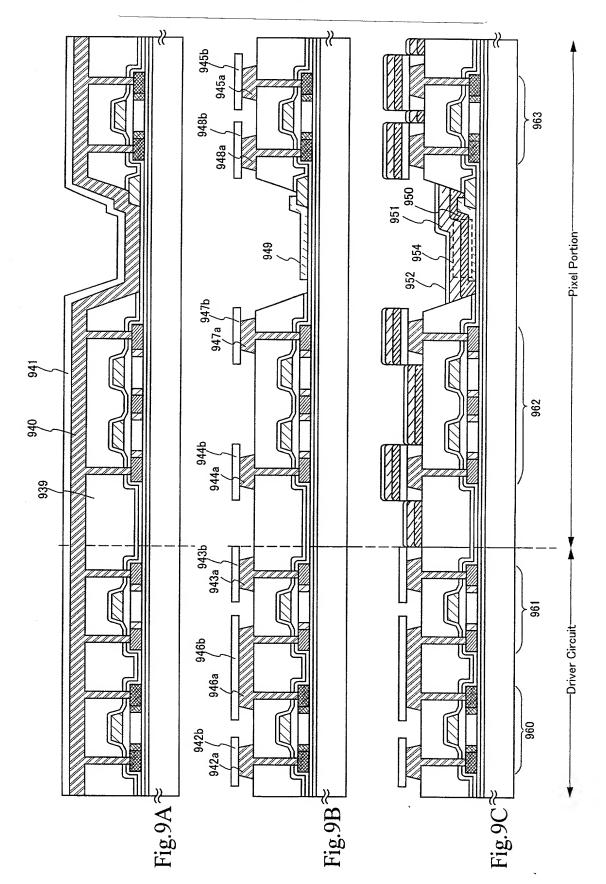


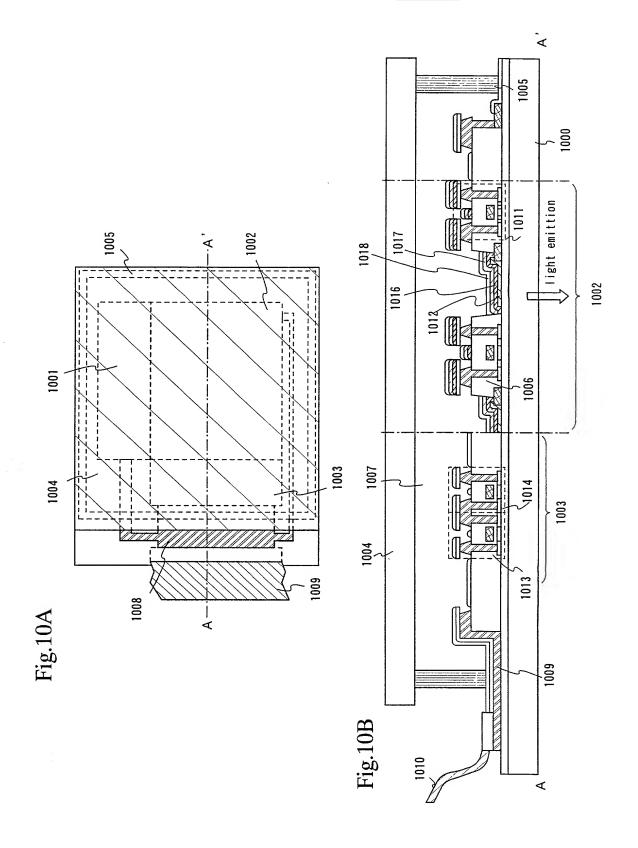




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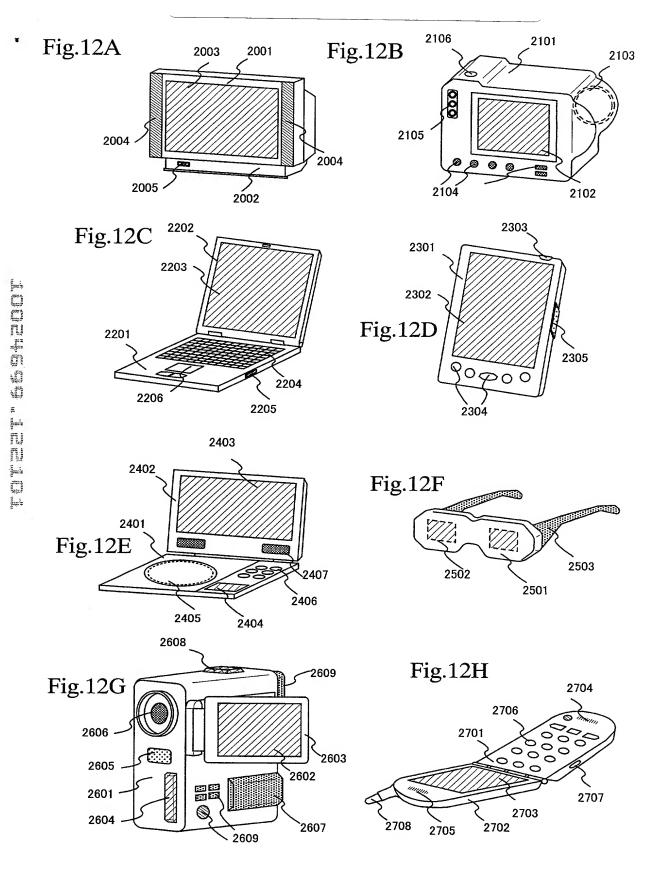


Fig.13A

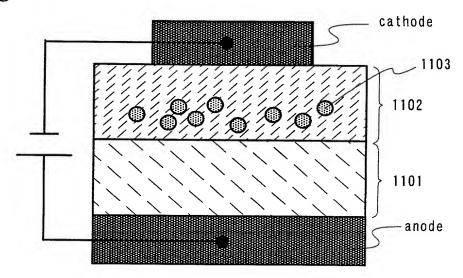


Fig.13B

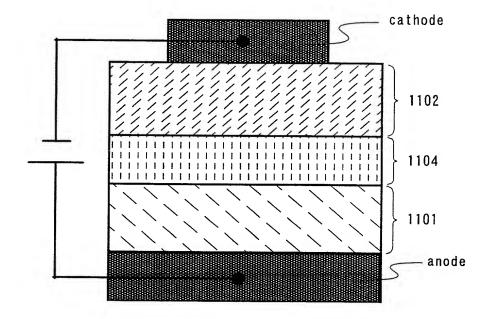
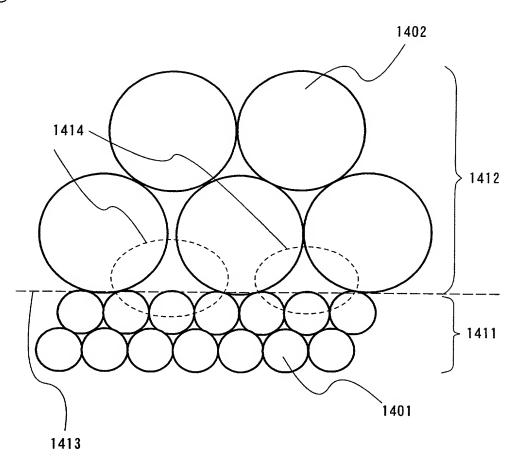


Fig.14



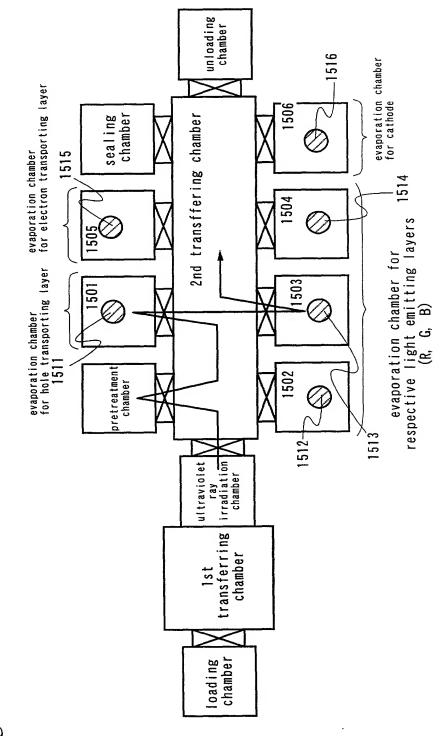


Fig.16

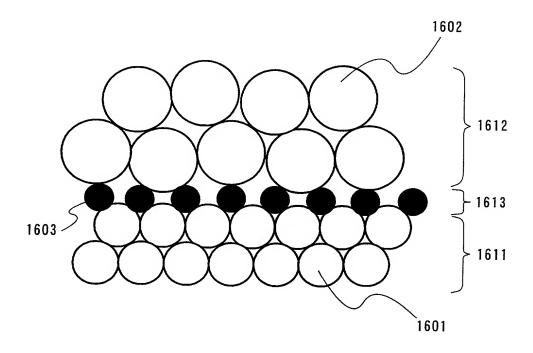
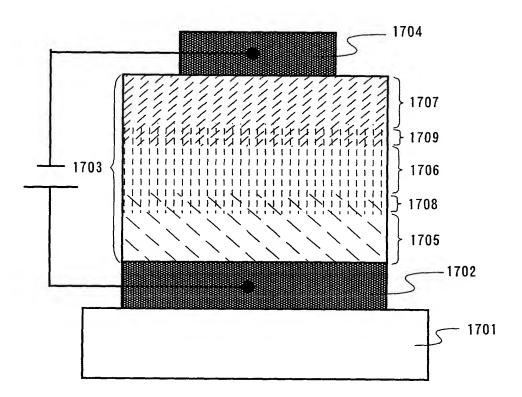
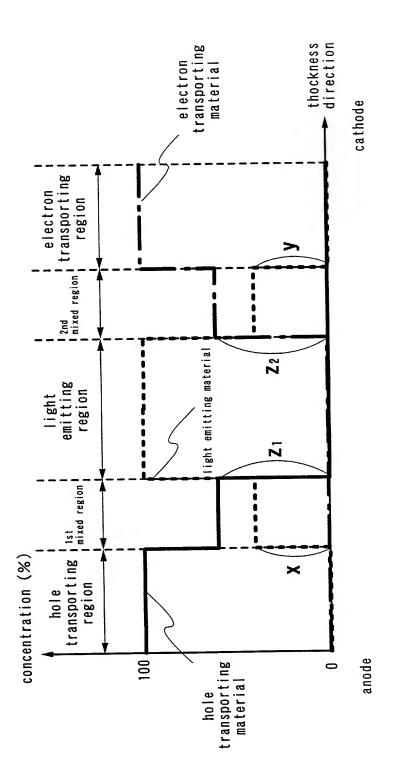


Fig.17





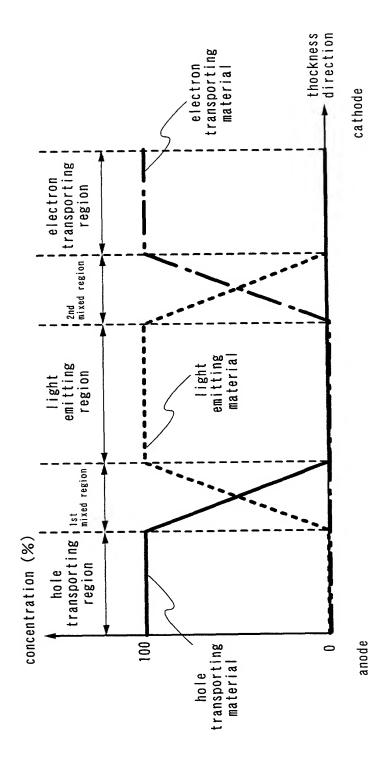
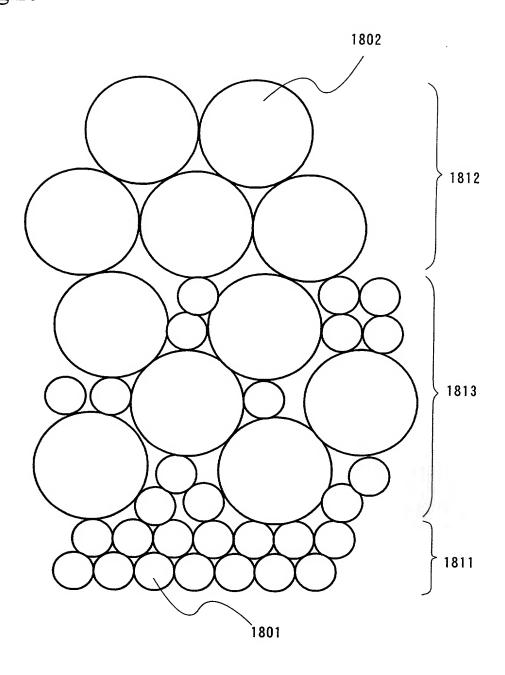


Fig.20



Applicant(s): Satoshi Seo et al.
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Fig.21A

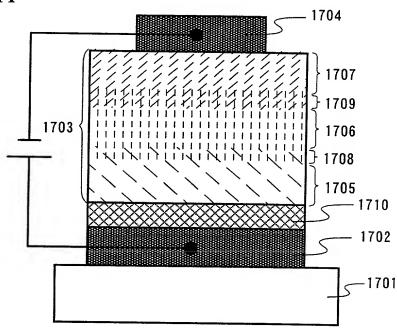
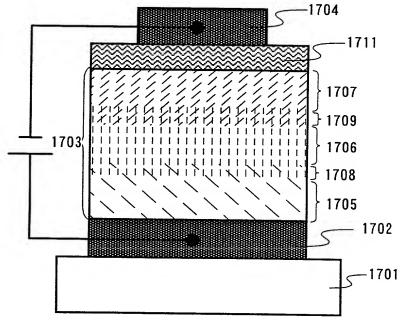
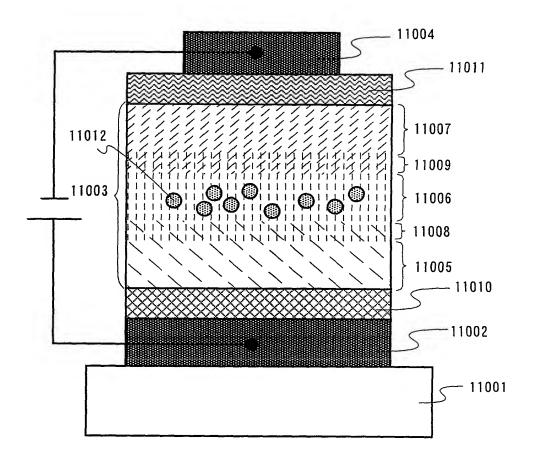


Fig.21B



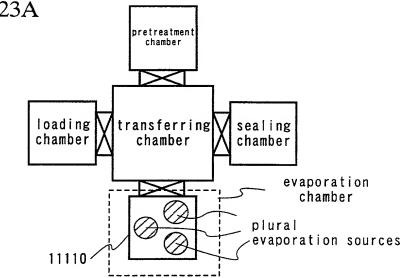
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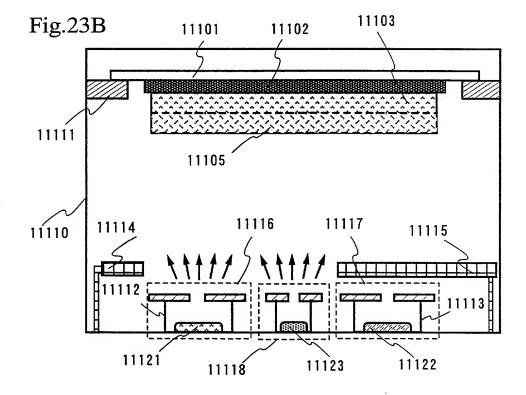
Fig.22

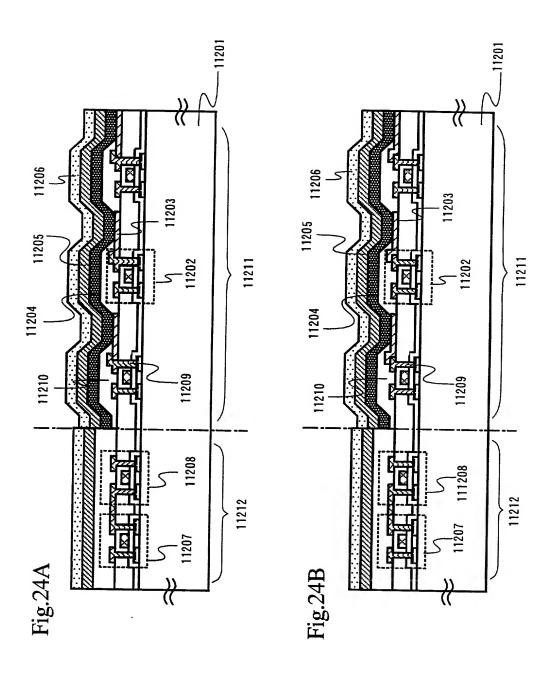


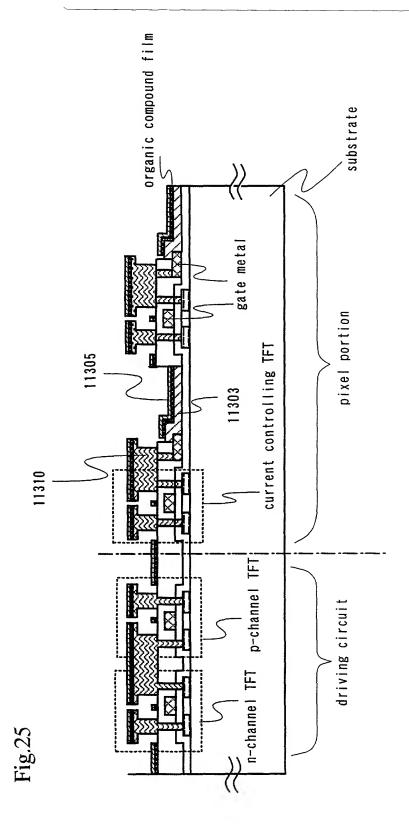
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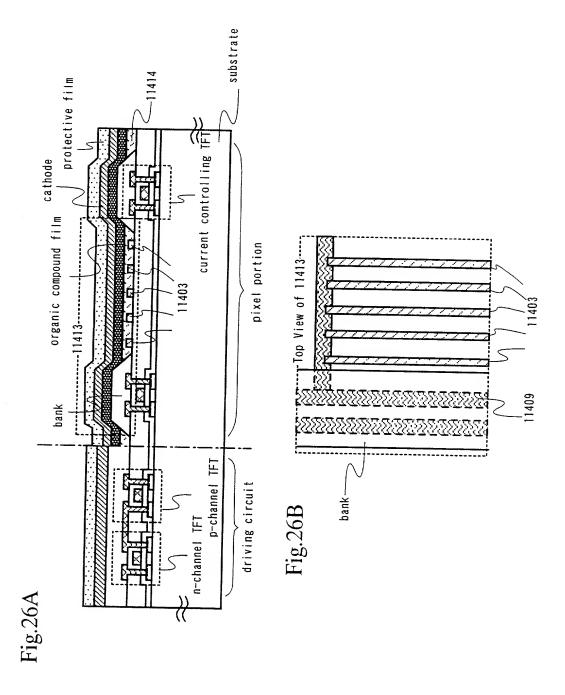




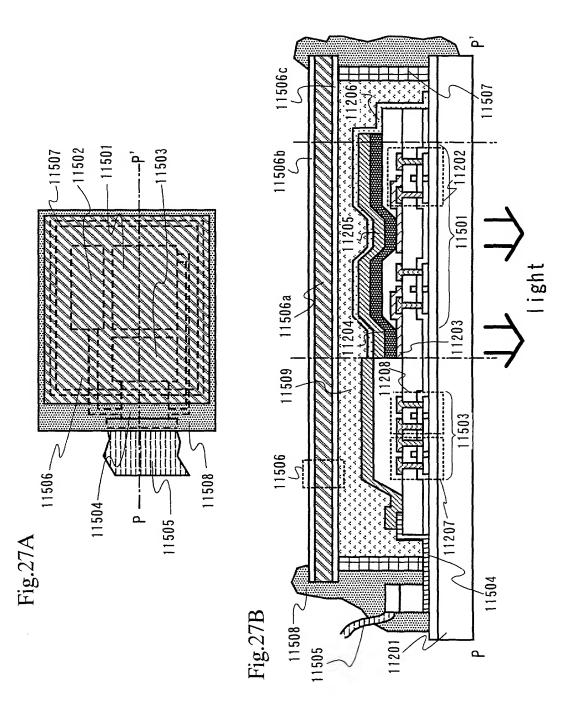


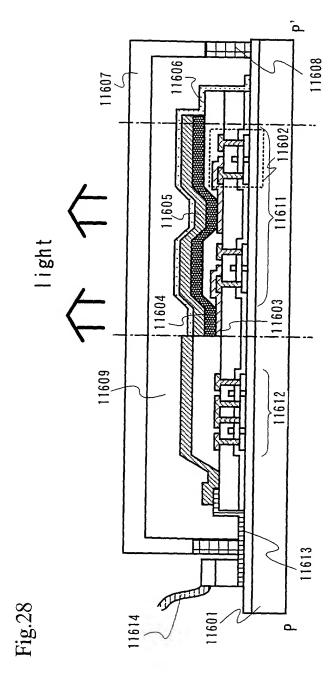


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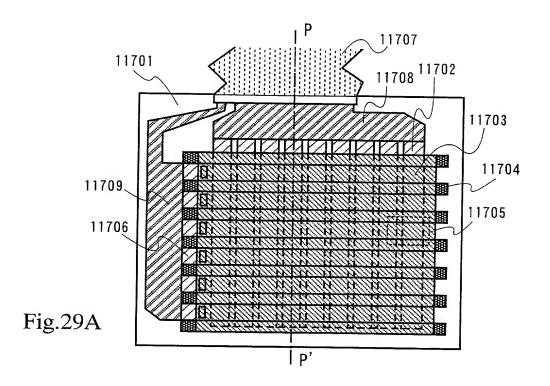


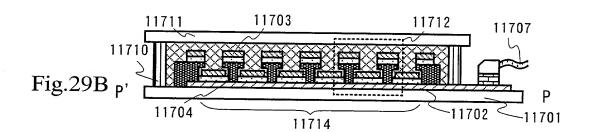


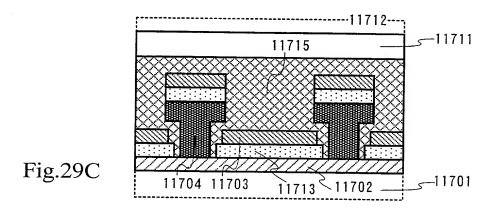












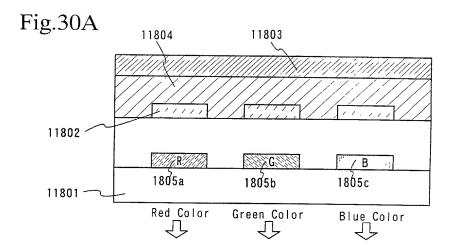


Fig.30B

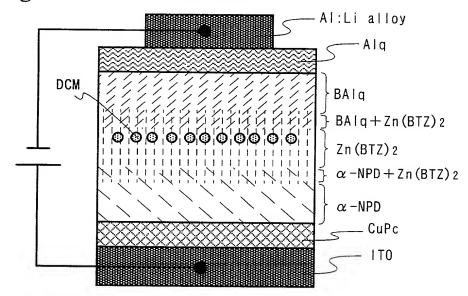


Fig.31A

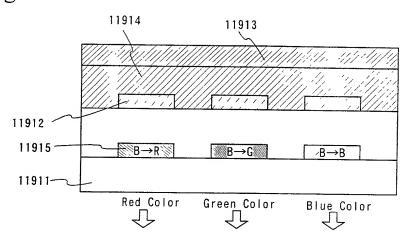
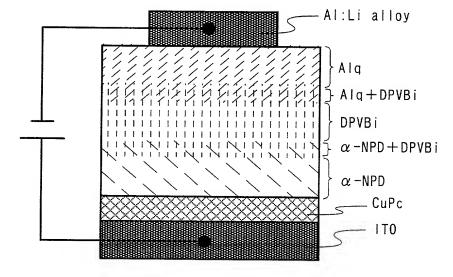


Fig.31B



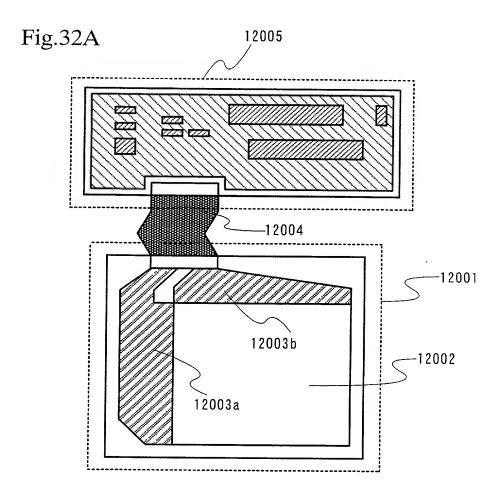


Fig.32B

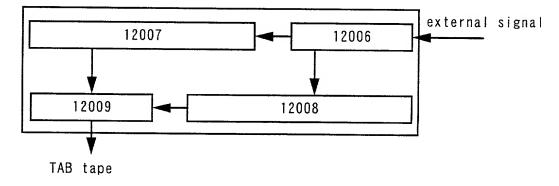


Fig.33A

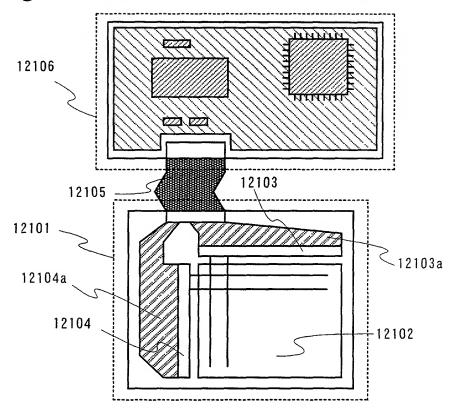
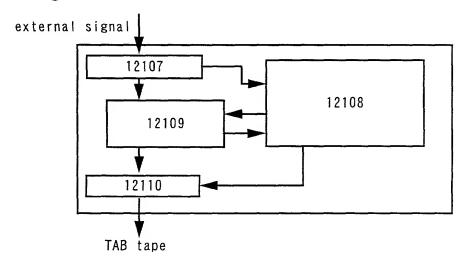
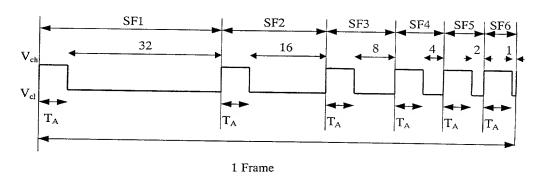


Fig.33B

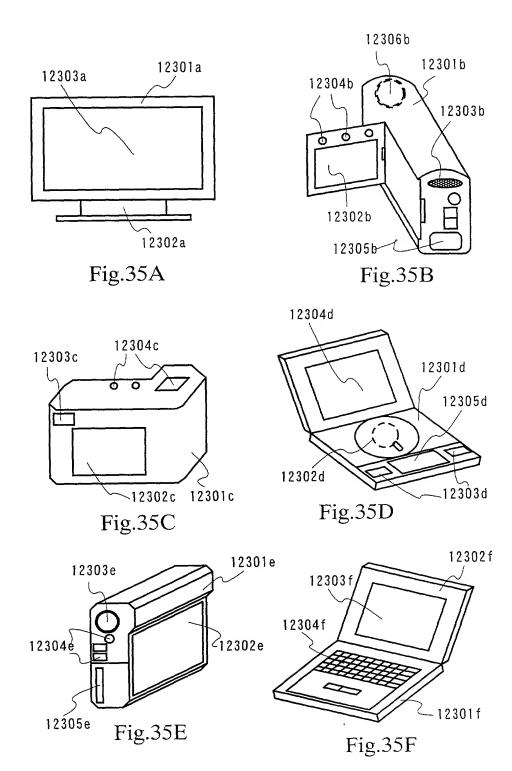


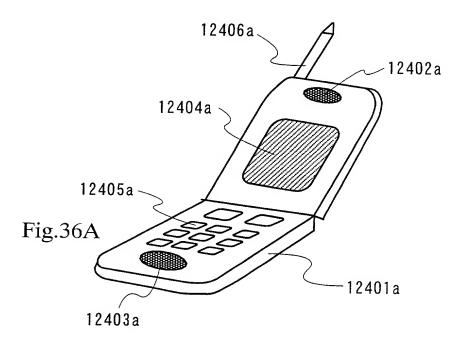
latch pulse digital signal source line gate line shift register pixel portion Tr1 organic light emitting cathod power supplying line cathode Fig.34A Fig.34B



SF1-SF6:sub-frame, TA: write in time

Fig.34C





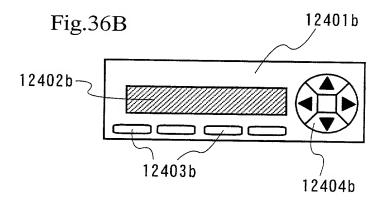


Fig.37

